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PTO/SB/21 (6-98)

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/904,578
	<b>Filing Date</b>	July 12, 2001
	<b>First Named Inventor</b>	Richard C. Eden et al.
	<b>Group Art Unit</b>	2812
	<b>Examiner Name</b>	Unknown
<b>Total Number of Pages in This Submission</b>	<b>Attorney Docket Number</b> IS9-017	

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
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<input type="checkbox"/> Amendment / Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	James D. Shaurette, Reg. No. 39,833 Wells St. John, P.S.
Signature	
Date	4/18/02

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 4-18-02	
Typed or printed name	Natalie King
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/904,578  
Filing Date . . . . . July 12, 2001  
Inventor . . . . . Richard C. Eden et al.  
Assignee . . . . . Isothermal Systems Research, Inc.  
Group Art Unit . . . . . 2812  
Examiner . . . . . Unknown  
Attorney's Docket No. . . . . IS9-017  
Title: Power Semiconductor Switching Devices, Power Converters, Integrated Circuit  
Assemblies, Integrated Circuitry, Power Current Switching Methods, Methods of Forming  
a Power Semiconductor Switching Device, Power Conversion Methods, Power  
Semiconductor Switching Device Packaging Methods, and Methods of Forming a Power  
Transistor



**INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449


Assistant Commissioner for Patents  
Washington, D.C. 20231

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The attached form PTO-1449 is submitted in compliance with  
37 CFR §1.56. No admission is made regarding whether all the submitted  
references are prior art.

Respectfully submitted,

Date: 4/18/02

By:   
James D. Shaurette  
Reg. No. 39,833  
WELLS, ST. JOHN, ROBERTS,  
GREGORY & MATKIN P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828  
(509) 624-4276